Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	19	(pipeline adj accelerator)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L2	1	pipeline (communication adj bus) clocks unsynchroniz\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND .	OFF	2008/01/30 17:10
L3	0	L2 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L4	1	pipeline bus clocks unsynchroniz\$4 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L5	2	L1 bus clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L6	1312	pipeline bus clock\$2 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L7	1287	L6 memory	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L8	6	L7 unsynchron\$6	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L9	0	L8 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L10	11	L1 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L11	11	L10 bus	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

			_	r · · · · · · · · · · · · · · · · · · ·		1
L12	11	L11 clock\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L13	736	L7 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L14	0	L8 hardwired	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L15	338	L13 clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L16	1014	L7 ((speed adj up) or fast\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L17	32	L7 (pipeline near3 ((speed adj up) or fast\$4))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L18	151	L15 hardwir\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L19	26	L18 router	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L20	26	L18 router\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L21	82	L18 (configuration adj data)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L22	63	L21 message\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L23	58	L22 ((accept\$2 or reject\$2) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

L24	57	L22 ((accept or reject) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L25	0	(hard-wire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L26	2	(hard-wire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L27	57	L23 buses	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L28	0	(hardwire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L29	1	"6023742".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND .	OFF	2008/01/30 17:10
L30	0	(hardwire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L31	1	"6282627".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L32	1312	pipeline bus clock\$2 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L33	1287	L32 memory	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L34	736	L33 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L35	338	L34 clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

					·	
L36	151	L35 hardwir\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L37	82	L36 (configuration adj data)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L38	1014	L33 ((speed adj up) or fast\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L39	6	L33 unsynchron\$6	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L40	0	L39 hardwired	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L41	0	(hardwire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L42	19	(pipeline adj accelerator)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L43	2	L42 bus clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L44	2	(hard-wire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L45	. 1	"6023742".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L46	1	pipeline (communication adj bus) clocks unsynchroniz\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L47	11	L42 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

L48	11	L47 bus	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L49	11	L48 clock\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L50	1	"6282627".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L51	63	L37 message\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L52	58	L51 ((accept\$2 or reject\$2) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L53	0	(hardwire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L54	0	(hard-wire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L55	57	L51 ((accept or reject) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L56	26	L36 router	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L57	1	pipeline bus clocks unsynchroniz\$4 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L58	57	L52 buses	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L59	32	L33 (pipeline near3 ((speed adj up) or fast\$4))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

L60	0	L39 asic	US-PGPUB; USPAT; USOCR;	AND	OFF	2008/01/30 17:10
L61	26	L36 router\$2	IBM_TDB US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L62	0	L46 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L63	0	asynchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L64	32	synchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L65	0	asynchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L66	32	synchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L67	0	asynchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L68	31	different near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L69	2	synchronize near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON .	2008/01/30 17:10
L70	0	unsynchronize near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L71	4	asynchron\$5 near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10

L72	0	unsynchronized near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L73	309	unsynchronized near2 clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L74	164	unsynchronized adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L75	15	(unsynchronized adj clock) and pipeline	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L76	106	(unsynchronized adj clock) and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L77	22	(unsynchronized adj clock) same processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L78	485	pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L79	0	unsynchronized adjpipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L80	0	asynchron\$5 adj adjpipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L81		asynchroniz\$3 adj adjpipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

L82	0	unsynchronized adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L83	1312	pipeline bus clock\$2 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L84	1287	L83 memory	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L85	736	L84 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L86	1	pipeline (communication adj bus) clocks unsynchroniz\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L87	0	L86 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L88	338	L85 clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L89	1312	pipeline bus clock\$2 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L90	1287	L89 memory	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L91	736	L90 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L92	338	L91 clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L93	151	L92 hardwir\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

L94	82	L93 (configuration adj data)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L95	63	L94 message\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L96	58	L95 ((accept\$2 or reject\$2) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L97	57	L96 buses	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L98	6	L84 unsynchron\$6	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L99	19	(pipeline adj accelerator)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L100	. 4	asynchron\$5 near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L101	0	(hardwire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L102	11	L99 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L103	32	L84 (pipeline near3 ((speed adj up) or fast\$4))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L104	6	L90 unsynchron\$6	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L105	0	L104 hardwired	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

,				T	1	
L106	151	L88 hardwir\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L107	19	(pipeline adj accelerator)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L108	11	L107 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L109	11	L108 bus	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L110	2	L99 bus clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L111	164	unsynchronized adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L112	106	(unsynchronized adj clock) and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L113	485	pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L114	0	unsynchronized adjpipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L115		"6282627".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L116	32	L90 (pipeline near3 ((speed adj up) or fast\$4))	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

			T		T	
L117	11	L102 bus	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L118	11	L117 clock\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L119	0	L98 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L120	0	unsynchronize near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L121	0	(hardwire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L122	2	(hard-wire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L123	82	L106 (configuration adj data)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L124	63	L123 message\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L125	58	L124 ((accept\$2 or reject\$2) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L126	57	L125 buses	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L127	0	asynchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L128	1	pipeline bus clocks unsynchroniz\$4 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10

L129	1	"6023742".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L130	0	(hardwire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L131	0	(hard-wire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L132	57	L95 ((accept or reject) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L133	26	L106 router\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L134	2073	(SCHULZ.in. and KENNETH.in.) or (RAPP.in. and JOHN.in.) or (JACKSON.in. and LARRY.in.) or (JONES.in. and MARK.in.) or (CHERASARO.in. and TRO.in.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L135	22	(unsynchronized adj clock) same processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L136	31	different near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L137	2	synchronize near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L138	15	(unsynchronized adj clock) and pipeline	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10

L139	0	(hardwire\$2 near5 FGPA)	US-PGPUB; USPAT;	AND	OFF	2008/01/30 17:10
		·	USOCR; IBM_TDB			
L140	11	L109 clock\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L141		L107 bus clocks	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L142	32	synchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L143	0	asynchron\$5 adj adjpipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L144	0	asynchroniz\$3 adj adjpipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L145	0	unsynchronized adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L146	1	pipeline (communication adj bus) clocks unsynchroniz\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L147	2	(hard-wire\$2 near5 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L148	32	synchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L149	0	asynchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L150	309	unsynchronized near2 clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10

L151	0	asynchron\$5 adj pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L152	1	"6023742".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L153	1	"6282627".pn.	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L154	1	pipeline bus clocks unsynchroniz\$4 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L155	26	L93 router	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L156	0	unsynchronized near2 pipeline adj clock	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	ON	2008/01/30 17:10
L157	1014	L90 ((speed adj up) or fast\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L158	0	L104 asic	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L159	0	L146 FPGA	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L160	247	712/15.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10
L161	376	712/34.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 17:10

						
L162	0	L98 hardwired	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L163	1014	L84 ((speed adj up) or fast\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L164	26	L106 router	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L165	26	L93 router\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L166	57	L124 ((accept or reject) near3 message\$2)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10
L167	0	(hard-wire\$2 near3 FGPA)	US-PGPUB; USPAT; USOCR; IBM_TDB	AND	OFF	2008/01/30 17:10